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Lou 13-13

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Hui-Ling Lou et al.  
Case: 13-13  
Serial No.: 09/390,389  
Filing Date: September 3, 1999  
Group: 2631  
Examiner: Kevin Michael Burd

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature: V. Burd Date: March 8, 2004

Title: Multiplier-Free Methods and Apparatus for Signal Processing in a Digital Communication System

RESPONSE TO FINAL OFFICE ACTION

Assistant Commissioner for Patents  
Washington, D.C. 20231

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Sir:

The remarks below are submitted in response to the final Office Action dated December 8, 2003, in the above-identified application. A Notice of Appeal is submitted concurrently herewith.

REMARKS

The present application was filed on September 3, 1999 with claims 1-25. Claims 6, 7, 17, 18, 23 and 25 were canceled in an amendment filed on March 7, 2003. Claims 1-5, 8-16, 19-22 and 24 are thus currently pending in the present application. Claims 1, 12 and 24 are the independent claims.

In the final Office Action, the Examiner rejected claims 1-5, 10-16, 21, 22 and 24 under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,233,271 (hereinafter "Jones") in view of pages 266-269 of a textbook by R.H. Katz entitled "Contemporary Logic Design" (hereinafter